

EV085432070

MAY 07 2003



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/050,348  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Schillinger, Laura M.  
Attorney's Docket No. .... MI22-1898  
Title: Transistor Structures

COPY

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

Dear Sir:

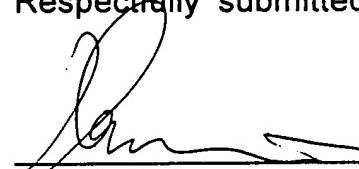
The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 with copies of non-patent literature attached.

Citation of these references is respectfully requested.

A check in the amount of \$180.00 is enclosed to cover the fee specified under 37 C.F.R. § 1.17(p).

Respectfully submitted,

Dated: 12/5/02

By: 

David G. Latwesen, Ph.D.  
Reg. No. 38,533

MAY 13 2003  
TECHNOLGY UNIT R2813  
RECEIVED

COPY

EV182657085



# 19  
3.26.04

EV085432070  
Malhotra

Inventoryurtej S. Sandhu et al.

Title: Transistor Structures

Assignee: Micron Technology, Inc.

Serial No.: 10/050,348

Filed: January 15, 2002 [RCE Filed Herewith]

**INFORMATION DISCLOSURE STATEMENT**

**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the references listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of these references is respectfully requested.

Date: 5/7/03

Attorney:

Respectfully submitted,

A handwritten signature in black ink, appearing to read "David G. Latwesen".

David G. Latwesen, Ph.D.  
Reg. No. 38,533  
Wells St. John P.S.

RECEIVED  
MAY 13 2003  
TECHNOLOGY CENTER 2800

EV085432070

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Application Serial No. .... 10/050,348  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Unknown  
Attorney's Docket No. .... MI22-1898  
Title: Transistor Structures

COPY

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(e) to Deposit Account No. 23-0925.

RECEIVED  
MAY 13 2003  
TECHNOLOGY CENTER 2800

Respectfully submitted,

By:

Jennifer J. Taylor, Ph.D.  
Reg. No. 48,711

Dated: 6-27-2002

COPY

EV077332375